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I PATENT ABSTRACTS OF JAPAN

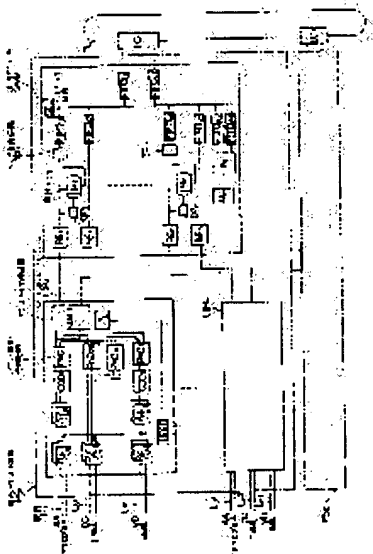
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(21)Application number : 62-001091 (71)Applicant : OKI ELECTRIC IND CO LTD
(22)Date of filing : 08.01.1987 (72)Inventor : UEDA HISAO

(54) INTEGRATED PACKET EXCHANGE SYSTEM



(57)Abstract:

PURPOSE: To attain information exchange between different media by one exchange by applying packet and multiplex processing according to the order of reception of various forms of information in sending the information from the caller side and restoring the information into the information of each form according to the sent order.

CONSTITUTION: An analog voice signal AA, a picture signal VD and a digital data signal D are subject to packet processing in a line interface module LIM1 via each reception/transmission circuit R/S and each packet composing/ decomposing circuit PAD. Then the signals are multiplexed sequentially by a packet multiplex conversion circuit MALT according to the order of reception and a line number is added to each header of the code string and sent to the switch module SWM.

Then the result is received by a reception circuit RE1 and each designation number to an incoming side switch module, a line interface module and a line is read from a

storage memory M1 as the information of a new header corresponding to the line number of the header and written in a memory FIFO11 together with the data information. Then the result is subject to switching control according to the designation of each number and converted and received after being converted into various information form.

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